

Applicant(s)	David J. Musliner et al.	<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT</p>
Serial No.	Unknown	
Filing Date	Herewith	
Group Art Unit	Unknown	
Examiner	Unknown	
Attorney Docket No.	H0001845	
Title: INCREMENTAL AUTOMATA VERIFICATION		

Jc858 U.S. PTO
 10/015058
 10/29/01

BOX PATENT APPLICATION

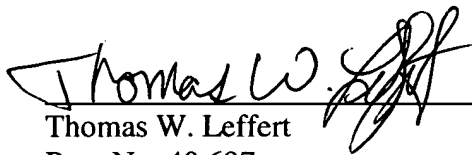
Commissioner for Patents
 Washington, D.C. 20231

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

Date: OCT 29, 01


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U.S. Patent References				
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Examiner Initials	Foreign Patent		Publication Date
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	R. Alur and D. Dill, "Automata-theoretic Verification of Real-Time Systems", November 2, 1995, pgs. 1-27.
	M. Archer and C. Heitmeyer, "Mechanical Verification of Timed Automata: A Case Study", 1996, pgs. 1-12.
	R. Goering, "Design Automation: Breakthrough claimed in functional verification", EE Times, May 21, 2001, pgs. 59 and 62.
	V. Gupta et al., "Hybrid Automata and Program Verification", 12 pgs.
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	N. Lynch and F. Vaandrager, "Forward and Backward Simulations Part II: Timing-Based Systems", April 26, 1995, pgs. 1-49.

Examiner Signature		Date Considered	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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	B. Plateau and W. Stewart, "Stochastic Automata Networks: Product Forms and Iterative Solutions", INRIA - 1996, pgs. 1-38.
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